



## Verification Engineer Job Description

Northwest Logic is looking for a full-time Verification Engineer to join the Northwest Logic team.

### Responsibilities:

- Verification planning, implementation and analysis
- RTL analysis and debug

### Required Skills:

- Strong verification expertise (System Verilog, testbench creation, functional/code coverage, use of third-party VIP, use of verification management tools, etc.)
- Questa/Incisive/VCS simulator experience
- Python/Perl/Tcl scripting experience
- Significant ASIC and/or FPGA verification experience
- Ability to learn quickly and work independently
- Solid communication and project management skills
- 5+ years of verification experience

### Definite Plus:

- UVM experience
- ASIC and/or FPGA design experience
- Memory (HBM/DDR/LPDDR), PCI Express, or MIPI expertise
- Located in the Portland, Oregon area

### Training:

- Northwest Logic will provide training as needed

For more information on Northwest Logic, please contact us at [info@nwlogic.com](mailto:info@nwlogic.com).