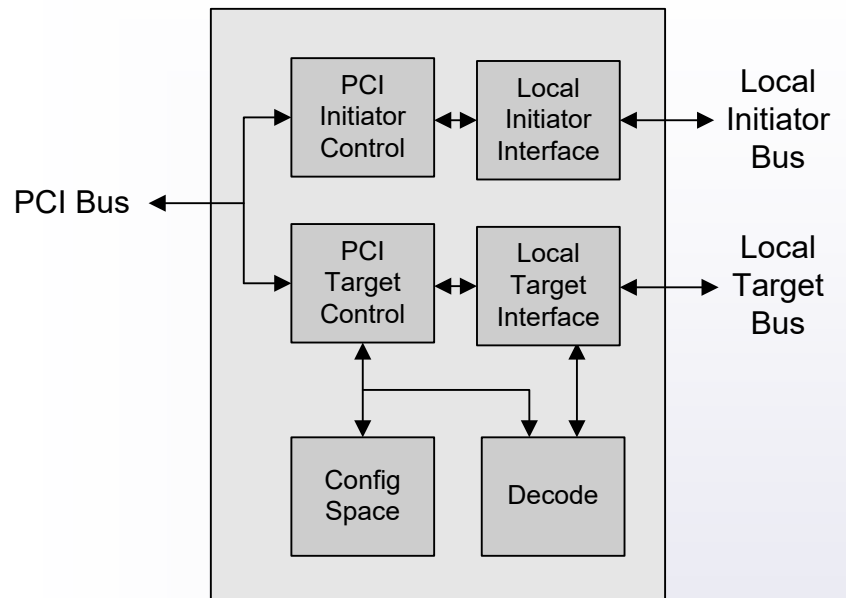


## Product Highlights

- High-performance, easy-to-use core
- PCI Local Bus Specification Revision 3.0 compliant
- Supports 33/66 MHz, 32/64 bit operation
- Master/Target and Target-Only versions
- Host and Peripheral versions available
- User expandable configuration space can be loaded from EEPROM
- Achieves push-button timing with minimal routing constraints
- Provided with a PCI Testbench
- Fully hardware validated
- Minimal ASIC gate count
- Broad range of ASIC and FPGA platforms supported
- Source code available
- Customization and Integration services available

## Block Diagram



## Product Overview

Northwest Logic's PCI Core has been specifically designed to be easy-to-use:

- Hides the complexities and timing issues of the PCI Bus from the user
- Control interface has consistent timing and function over all modes of operation
- Data interface connects directly to FIFOs
- Provides complete error-handling support
- Achieves push-button timing with minimal routing constraints

The PCI Core implements the "front-end" of a PCI design giving the user complete control of the "back-end" functionality. This flexibility is further enhanced with a user expandable configuration space. This PCI Core supports automatic loading of the configuration space via a EEPROM.

The core is available in several flavors including 32 vs. 64 bit, Target-Only vs. Master/Target, and Peripheral vs. Host.

The core is also provided with the PCI Testbench which pro-

vides a PCI Bus Functional Model.

Northwest Logic also provides IP Core customization services. Contact Northwest Logic for a quote.

### Product Deliverables:

- Core (Netlist or Source Code)
- Testbench (Source Code)
- Complete Documentation
- Expert Technical Support & Maintenance Updates