**Solution Includes:**

- **Memory Controller Cores**
  - Supports HBM2/2E, GDDR6, DDR4/3, LPDDR4
  - 64 banks and 16 deep command queue support
  - Supports all memory configurations including multi-rank, DIMM, RDIMM, LRDIMM etc.
  - Supports Full Rate, Half Rate and Quarter Rate operation
  - Provided with Memory Controller testbench

- **Add-On Cores**
  - Supports AXI, OCP Interfaces
  - Multi-port, priority-based arbitration support
  - Efficiency-based reordering maximizes memory utilization
  - Out-of-Band and In-Line ECC support
  - Comprehensive Memory Test Package supports chip and board bring-up

- **DDR PHY**
  - Fully integrated with a broad variety of third-party PHYs

**Key Features:**

- **Complete Solution**
  - Full featured and modular solution enables IP to be configured to exact customer requirements
  - Flexible PHY interface supports all DFI compatible PHYs
  - Delivered fully integrated and verified with target DDR PHY
  - Minimal size relative to competition
  - JEDEC standard compliant
  - Silicon proven

- **High Performance**
  - Supports maximum data rates specified by the JEDEC standards
  - Supports high-efficiency, multi-port operation
  - Minimal latency

- **Easy-To-Use**
  - Simple interface, easy to configure, well-documented

- **Comprehensive Support**
  - ASIC, Structured ASIC and FPGA support
  - Expert technical support provided directly by designers
  - Integration and customization services available