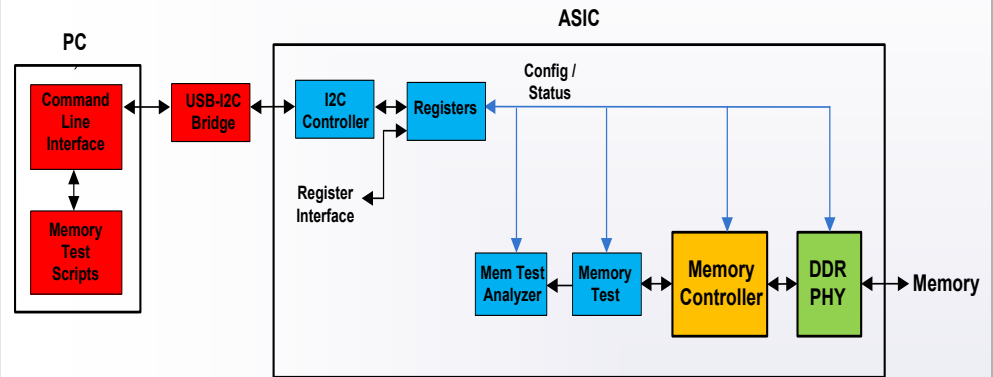


Product Highlights

- Part of Northwest Logic's comprehensive Memory Test Package
- Capture the results from Northwest Logic's Memory Test Core.
- Data retrieved via chip's Configuration & Status bus, on-chip processor or dedicated low pin-count serial port
- Easy-to-use software including scripts and driver available
- Provides a low-cost, built-in logic analyzer capability
- Useful for chip and board validation
- Minimal ASIC gate count
- Broad range of ASIC and FPGA platforms supported
- Source code available
- Customization and Integration services available

Block Diagram



Product Overview

The Mem Test Analyzer Core is used to capture the results from Northwest Logic's Memory Test Core.

The Mem Test Analyzer Core can be used in conjunction with the Memory Test Core to capture the actual and expected test data. The capture is initiated by an error trigger signal provided by the Memory Test Core. This data can then be retrieved from the Mem Test Analyzer Core via the chip's configuration & status bus, on-chip processor or dedicated low-pin count serial port.

Northwest Logic also offers easy-to-use scripts, driver and USB-I2C bridge board to retrieve and analyze the data captured by the Mem Test Analyzer Core.

The core is useful for chip and board validation. It provides low-cost, built-in logic analyzer capability similar in concept to the FPGA-based internal logic analyzer tools.

Northwest Logic also provides IP Core customization services. Contact Northwest Logic for a quote.

Product Deliverables:

- Core (Netlist or Source Code)
- Testbench (Source Code)
- Complete Documentation
- Expert Technical Support & Maintenance Updates