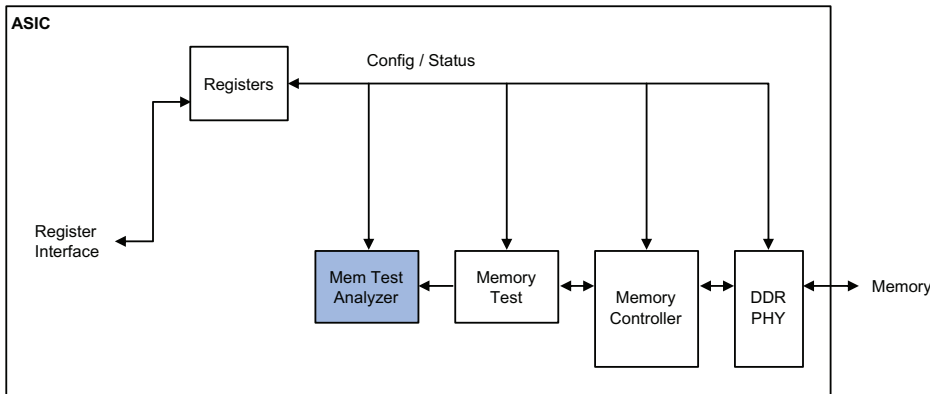


Mem Test Analyzer Core

The Northwest Logic Mem Test Analyzer core is used to capture the results from the Northwest Logic Memory Test core.

Mem Test Analyzer Core Application Example



Overview

Northwest Logic's Mem Test Analyzer Core is used to capture the results from Northwest Logic's Memory Test Core.

The Mem Test Analyzer Core can be used in conjunction with the Memory Test Core to capture the actual and expected test data. The capture is initiated by an error trigger signal provided by the Memory Test Core. This data can then be retrieved from the Mem Test Analyzer Core via the chip's configuration and status bus, on-chip processor or dedicated low-pin count serial port.

The core is useful for chip and board validation. It provides low-cost, built-in logic analyzer capability similar in concept to the FPGA-based internal logic analyzer tools.

Northwest Logic also provides IP Core customization services.

Highlights

- Part of comprehensive memory test package
- Capture results from Northwest Logic Memory Test core
- Data retrieved via chip's configuration and status bus, on-chip processor or dedicated low pin-count serial port
- Easy-to-use software including scripts and driver included
- Provides low-cost, built-in logic analyzer capability
- Useful for chip and board validation
- Minimal ASIC gate count
- Source code available
- Customization and integration services available

Deliverables

- Core (source code)
- Testbench (source code)
- Complete documentation
- Expert technical support
- Maintenance updates

rambus.com/controllers

