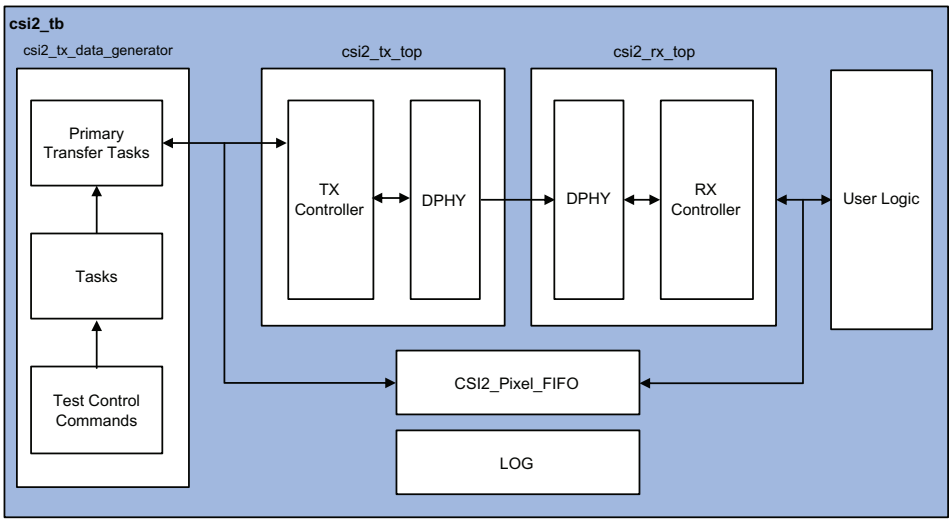
MIPI Testbench

The Northwest Logic MIPI Testbench emulates a MIPI device enabling end-to-end simulation of a MIPI design.

MIPI Testbench Block Diagram (Transmit Version)



Highlights

- Emulates MIPI device enabling end-to-end MIPI design simulation
- CSI-2, DSI and DSI-2 compliant versions
- Transmit (Host) and Receive (Peripheral) versions
- Performs automatic data logging and checking
- Includes basic set of test cases
- Quick out-of-the-box setup and use
- Fast simulation speed
- Provided as source code

Deliverables

- Testbench (source code)
- Complete documentation
- Expert technical support
- Maintenance updates

Overview

The Northwest Logic MIPI Testbench emulates a MIPI device enabling end-to-end simulation of a MIPI design.

This includes the follow features:

- Separate versions for CSI-2 Transmit, CSI-2 Receive, DSI-2 Host (Transmit), DSI-2 Peripheral (Receive), DSI Host, and DSI Peripheral
- Logging and display of all MIPI traffic in a user-friendly format
- Extensive set of test scripts included

Fast setup and operation of the MIPI Testbench makes it easy to quickly and comprehensively perform end-to-end simulations of MIPI designs. The Testbench is fully compliant with CSI, DSI and DSI-2 specifications.

The MIPI Testbench is not a full MIPI compliance suite. Contact Rambus for recommended third-party MIPI compliance suites for ASIC validation.

Features

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- CSI-2, DSI and DSI-2 compliant versions
- Transmit (Host) and Receive (Peripheral) versions
- Performs automatic data logging and checking
- Includes basic set of test cases
- Quick out-of-the-box setup and use
- Fast simulation speed
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