



Logic Designer Job Description

Northwest Logic is looking for a full-time Logic Designer to join the Northwest Logic team.

Responsibilities:

- Design architecting and trade-off analysis
- RTL coding and verification
- Controller + PHY integration and verification
- FPGA targeting
- Customer delivery and support

Required Skills:

- Strong Verilog RTL design and verification expertise
- Questa/Incisive/VCS simulator experience
- Python/Perl/Tcl scripting experience
- Significant ASIC and/or FPGA design experience
- Ability to learn quickly and work independently
- Solid communication and project management skills
- 5+ years of logic design experience

Definite Plus:

- ASIC synthesis, timing constraint, CDC/RDC experience
- Memory (HBM/DDR/LPDDR), PCI Express, or MIPI expertise
- Located in the Portland, Oregon area

Training:

- Northwest Logic will provide training as needed

For more information on Northwest Logic, please contact us at info@nwlogic.com.