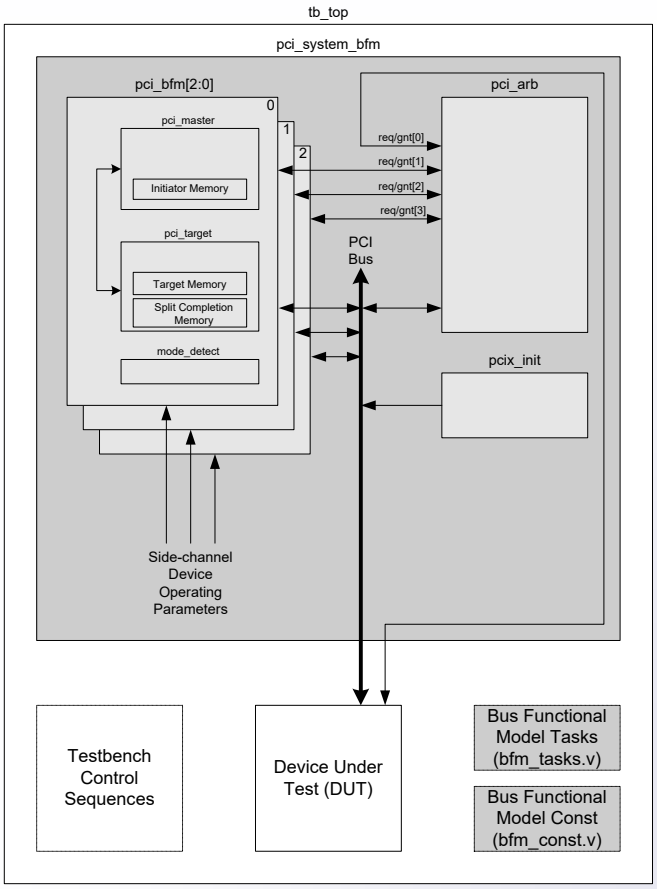
PCI-X / PCI Testbench

Product Highlights

- PCI-X/PCI Bus Functional Model
- Dynamically programmable probabilities for flow control signaling (master/target wait states, retries, disconnects, split responses, etc.)
- Includes models for master and target behaviors and a PCI-X/PCI bus arbitor
- Includes high-level tasks calls for initiating various forms of bus traffic including host configuration and test pattern generation
- Provided with a basic set of test cases.
- PCI Local Bus Specification Revision 3.0 and PCI-X Local Bus Specification Revision 2.0 (Mode 1) compliant
- Provided in clear text source

Block Diagram



Product Overview

Northwest Logic's PCI-X/PCI Testbench provides a PCI-X/PCI Bus Functional Model.

The standard environment includes three instances of device bus functional models, enabling simulation of multi-device, high traffic conditions. The environment also includes a PCI-X / PCI arbitor and bus mode detection circuits.

High level task calls are provided to enable generation and of complex bus traffic with regressive checking of data integrity. Tasks are also included which perform host configuration of devices on primary and secondary busses.

Each bus functional model instance contains parameterized memory for master and target side transactions. Additionally memory is provided for up to 32 tags of split completions. The initiator model automatically transfers data between target and master memory for split completions.

Each bus functional model is programmed with probabilities for modeling various target behaviors including target wait states, retries, disconnects, split responses, and 64/32-bit transaction conversion. Each bus functional model can be independently programmed for any decode speed.

A basic set of testbench control sequences are provided, demonstrating initiation of transactions and control of bus functional model target behavior.

The Testbench is compliant with PCI Local Bus Specification Revision 3.0 and PCI-X Local Bus Specification Revision 2.0 (Mode 1).

Product Deliverables:

- Testbench (Source Code)
- Documentation
- Expert Technical Support & Maintenance Updates