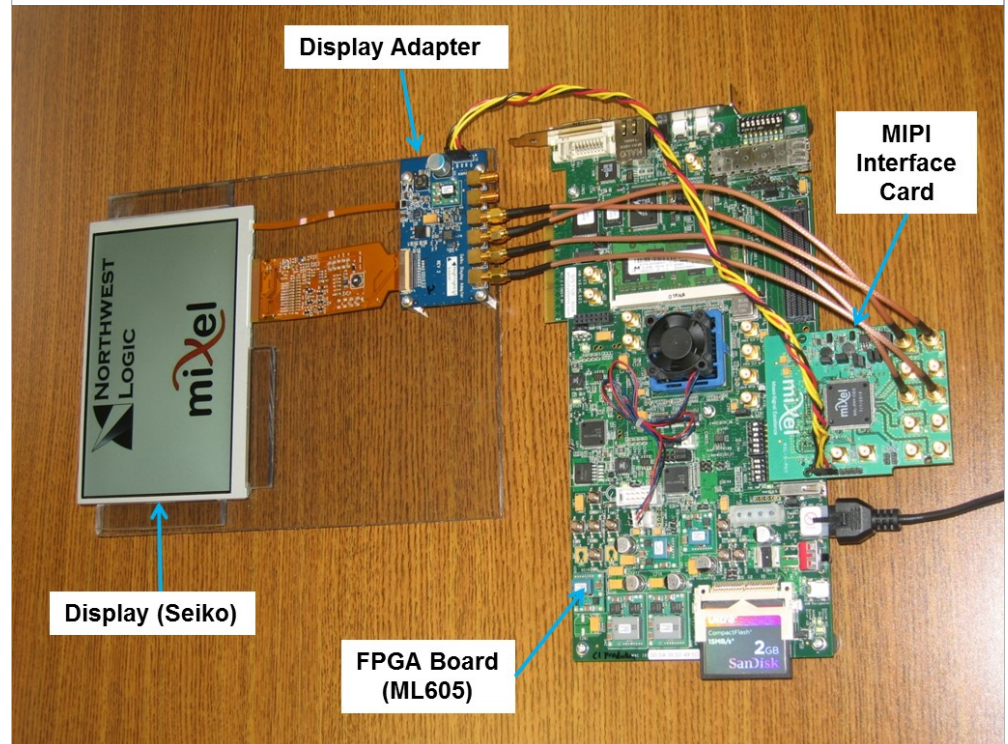


Product Highlights

- Complete DSI Demonstration and ASIC Prototyping Platform
- Supports DSI operation using Toshiba DSI-RGB Display Bridge Chip and Seiko Display (70WW2A)
- Utilizes MIPI Interface Card with PHY Test Chip from Mixel
- Works with a wide variety of FPGA Mezzanine Connector (FMC) based FPGA boards
- Provided with complete FPGA reference design

Block Diagram



Product Overview

Northwest Logic and Mixel provide a complete DSI Demonstration System and ASIC Prototyping Platform. This system includes the following components:

Display (Seiko 70WW2A)

- WVGA (800 x 480) display

Display Adapter Card (Northwest Logic)

- Provides an interface between the target Display and the MIPI Interface Card
- Uses Toshiba DSI-RGB Display Bridge Chip (TC358762XGB)

MIPI Interface Card (Mixel)

- This card contains Mixel's PHY Test Chip. Mixel is a leading provider of MIPI PHYs for use in ASICs.
- Connects to FPGA board using a High Pin Count (HPC) FPGA Mezzanine Connector (FMC)

FPGA Board (Various)

- A wide variety of HPC FMC-based FPGA Boards are available

FPGA Reference Design (Northwest Logic)

- This design includes Northwest Logic's DSI Controller Cores along with additional logic needed for a complete DSI Demonstration

Contact Northwest Logic for more information including addi-

tional camera/display support, pricing and availability.

Product Deliverables:

- Display
- Display Adapter Card
- MIPI Interface Card
- FPGA Reference Design *
- Expert Technical Support & Maintenance Updates

* FPGA Board is typically supplied by the End User